

## Elec 2200 Digital Logic Circuits

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### Elec 2200 Digital Logic Circuits

ELEC 2200 Digital Logic Circuits. Lecture: 2:00-2:50pm MWF Broun 239 General Syllabus . Spring 2013 Syllabus. The make-up exam for Test#3 will be 4-5pm Friday April 26 in Broun 314 for those students with excused absences covered by Paragraph 4 of the AU "Policy on Class Attendance"

### ELEC 2200 Digital Logic Circuits - Auburn University

ELEC 2200 Digital Logic Circuits Charles E. Stroud, Professor Dept. of Electrical & Computer Engineering Office: 325 Broun Hall ... digital circuits: • Logic gates: AND, OR, NOT voltage time voltage time 0 1 0 1 1. C. E. Stroud Intro to Digital Systems (1/06) 6 Elementary Logic Gates Input Output 1 0 0 1

### ELEC 2200 Digital Logic Circuits - Auburn University

ELEC 2200 - DIGITAL LOGIC CIRCUITS. Fall Semester, 2020. Instructor: Victor P. Nelson, Broun326, nelsovp@auburn.edu. Class Meetings: Monday 2:00-2:50, Broun 239. Wednesday and Friday 2:00-2:50 Brown-Kopel2143. Course Syllabus. Homework assignments will be postedon Canvas:

### ELEC 2200 - DIGITAL LOGIC CIRCUITS - Auburn University

ELEC 2200 - DIGITAL LOGIC CIRCUITS (Required for ECPE, Required for ELEC) 2004 Catalog Data: ELEC 2200. DIGITAL LOGIC CIRCUITS (3) LEC. 3. Pr., COMP 1200. Electronic devices and digital circuits; binary numbers; Boolean algebra and switching functions; gates and flip-flops; combinational and sequential logic circuits;

### ELEC 2200 - DIGITAL LOGIC CIRCUITS - Auburn University

ELEC 2200 at Auburn University (Auburn) in Auburn, Alabama. Electronic devices and digital circuits; binary numbers; Boolean algebra and switching functions; gates and flip-flops; combinational and sequential logic circuits; hierarchical design of digital systems; computer-aided design tools for digital design, simulation, and testing.

### ELEC 2200 - Digital Logic Circuits at Auburn University ...

algebra, which you studied in depth in ELEC 2200 Digital Logic Circuits. Logic Gates We will introduce the most common logic gates in this section, including the AND, OR, XOR, NOT, NOR, and NAND. For each gate, we will show the circuit symbol, the Boolean algebra logic function, and the truth table.

### ELEC 2210 - EXPERIMENT 1 Basic Digital Logic Circuits

ELEC 2200 - DIGITAL LOGIC CIRCUITS class wall and course overview (exams, quizzes, flashcards, and videos) at Auburn (AU)

### ELEC 2200: DIGITAL LOGIC CIRCUITS: Auburn (AU): Koofers

Review of ELEC 2200 - Digital Circuits and Systems Design of combinatorial and sequential logic circuits; introduction to logic families (TTL and CMOS); programmable logic devices; special digital systems. Laboratory assignments make extensive use of computer-aided design (CAD) tools for design, simulation and testing. Course Review >

### Review of ELEC 2200 - Digital Circuits and Systems ...

ELEC 2200 - DIGITAL LOGIC CIRCUITS free online testbank with past exams and old test at Auburn (AU)

### Test Bank: ELEC 2200: Auburn (AU): Koofers

The range of voltages corresponding to Logic Low is represented with '0'. Similarly, the range of voltages corresponding to Logic High is represented with '1'. The basic digital electronic circuit that has one or more inputs and single output is known as Logic gate. Hence, the Logic gates are the building blocks of any digital system.

### Digital Circuits - Logic Gates - Tutorialspoint

ELEC 2200 Digital circuit . School: Hong Kong University of Science and Technology (HKUST) \* Professor: ... Lab6 - Flip-Flops and Sequential Logic II. 29 pages. Lab7 - OrCAD PLD Tools using VHDL design entry HKUST Digital circuit ELEC 2200 - Fall 2012 ...

### ELEC 2200 : Digital circuit - HKUST

ELEC 2200-001 Digital Logic Circuits Fall 2010 Delay and Power - ELEC 2200-001 Digital Logic Circuits Fall 2010 Delay and Power Vishwani D. Agrawal James J. Danaher Professor Department of Electrical and Computer Engineering | PowerPoint PPT presentation | free to view

### PPT - ELEC 2200-003 Digital Logic Circuits Fall 2011 ...

ELEC 2200-001 Digital Logic Circuits Fall 2010 Logic Synthesis (Chapters 2-5) Description: ELEC2200-001 Lecture 6 \* Programmable Logic Array (PLA) A direct implementation of multi-output function as a two-level circuit in MOS technology.

### PPT - ELEC 2200-001 Digital Logic Circuits Fall 2010 Logic ...

View Notes - Syllabus\_M2011 from ELEC 2200 at Auburn University. ELEC 2200 - DIGITAL LOGIC CIRCUITS SUMMER SEMESTER - 2011 2011 Catalog Data: ELEC 2200. DIGITAL LOGIC CIRCUITS (3). Prereq. COMP 1200

### Syllabus\_M2011 - ELEC 2200 DIGITAL LOGIC CIRCUITS SUMMER ...

elec 2200-002 digital logic circuits spring 2017 introduction murali venkatasubramanian department of electrical and computer engineering auburn university, auburn, al 36849 COURSE ORGANIZATION • TEXT BOOK: V. P. NELSON, H. T. NAGLE, B. D. CARROLL AND J. D. IRWIN, DIGITAL LOGIC CIRCUIT ANALYSIS AND DESIGN, PRENTICE HALL, 1995, ISBN 0-13-463894-8.

### lec1\_intro.pptx - ELEC 2200-002 DIGITAL LOGIC CIRCUITS ...

Review your class notes from ELEC 2200 and then design and sketch a circuit diagram of a 3-bit synchronous binary counter using only three JK flip flops (use the JK flip flops in TTL standard part SN74LS73) and two 2-input AND gates (from TTL standard part 74LS08). Refer to Section 7.3.1 and Figure 7.11 of the ELEC 2200 text (Digital Logic Circuit Analysis & Design, by Nelson, et. al.) for a discussion and example of this counter circuit.

### Solved: Review Your Class Notes From ELEC 2200 And Then De ...

ELEC 2200-003 Digital Logic Circuits Final Exam, December 8, 2011 Total 20 points Broun 113, 4:00-6:30PM Instructions: Please read all problems before writing your answers. Attempt all six (6) problems. Be sure to revise your answers before turning them in. Please number your answer sheets, and on the first page identify the exam as shown above, write your name and the total number of pages ...

### Exam\_2200\_sol - ELEC 2200-003 Digital Logic Circuits Final ...

View Notes - hw3.sol from ELEC 2200 at Auburn University. ELEC 2200-003 Digital Logic Circuits Fall 2011 Homework 3 Solution Assigned 9/12/11, due 9/19/11 Problem 1: For 3-bit 2s complement binary

### hw3.sol - ELEC 2200-003 Digital Logic Circuits Fall 2011 ...

then how digital logic functions are constructed using those gates. The concept of memory is then introduced through the construction of an SR latch and then a D flip-flop. A clock is created to be used in a basic state machine design that aims to combine logic circuits with memory. Target audience

### Introduction to Digital Logic with Laboratory Exercises

Understanding digital logic gates is a major prerequisite to learning how all digital electronic circuits work. Basically, there are three types of electronic "logic gates:" the AND gate, the OR gate, and the NOT gate. (Actually, there are a few more types of gates, which we will cover later in this article.) Just remember, logic gates are ...